Product / Process Change Notification

N° 2015-064-A

Dear Customer,

Please find attached our INFINEON Technologies PCN:

XMC4100 and XMC4200: Introduction of Taiwan Semiconductor Manufacturing Company (TSMC) as wafer fab location and ASE, Taiwan and AMKOR, Philippines as assembly location.

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before **04. March 2016**.
- Infineon aligns with the widely-recognized JEDEC STANDARD “JESD46“, which stipulates: “Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change.“

Your prompt reply will help Infineon Technologies to assure a smooth and well executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.

Disclaimer:
If we do not receive any response within the given time limit we consider this as the acceptance of the PCN.

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SUBJECT OF CHANGE: XMC4100 and XMC4200: Introduction of Taiwan Semiconductor Manufacturing Company (TSMC) as wafer fab location and ASE, Taiwan and AMKOR, Philippines as assembly location.

PRODUCTS AFFECTED: Please refer to 1_cip15064_a attached.

REASON OF CHANGE: In alignment with Infineon's subcon strategy.

DESCRIPTION OF CHANGE:	OLD	NEW
■ **Wafer fab location /**	Infineon Technologies Dresden, Germany	Taiwan Semiconductor Manufacturing Company (TSMC), Taiwan
Wafer test location:	Infineon, Dresden	Ardentec, Taiwan
Assembly location:	Infineon Malacca, Malaysia	AMKOR, Philippines (affecting package VQFN-48)
	Infineon Malacca, Malaysia	ASE, Taiwan (affecting package TQFP-64)
■ **SP-number:**	refer to 3_cip15064_a	refer to 3_cip15064_a
■ **Marking:**	Example: XMC4200-Q48K256 **AB**	Example: XMC4200-Q48K256 **BA**
■ **Package version:**	PG-VQFN-48-53 PG-LQFP-64-19	PG-VQFN-48-71 PG-TQFP-64-19
■ **Data Sheet:**	V 1.2	V 2.3 (refer to 4_cip15064_a)

PRODUCT IDENTIFICATION:

- On Product Level:
  - Identification via lot number
  - Different SP numbers

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TIME SCHEDULE:

■ Final qualification report:	Available
■ First samples available:	Available
■ Intended start of delivery:	April 2016

ASSESSMENT:

Based on the qualification performed, Infineon does not expect any negative impact on quality, function and reliability.

DOCUMENTATION:

1_cip15064_a: products affected
3_cip15064_a: list of SP numbers old vs new
4_cip15064_a: datasheet (V 1.3)